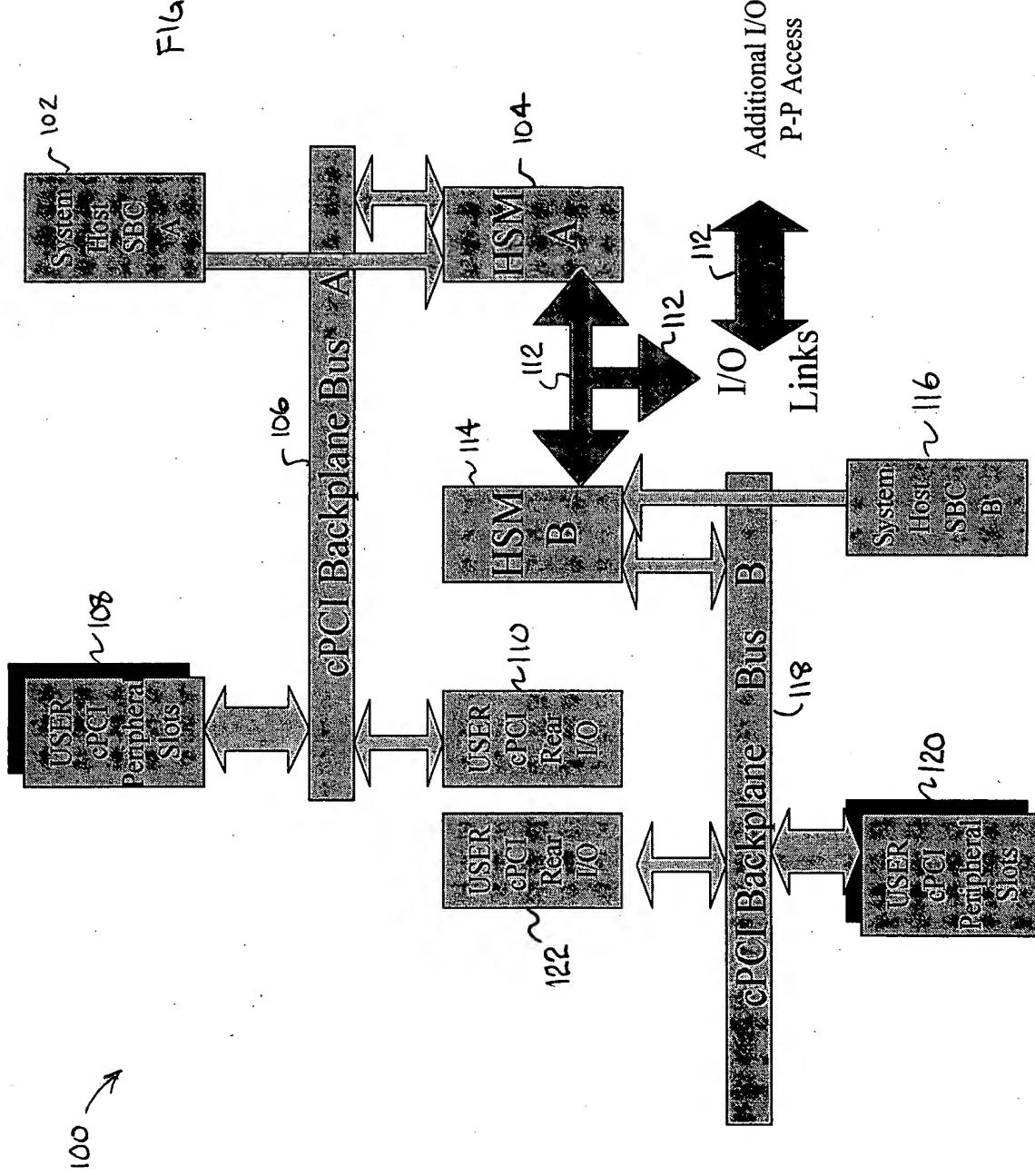


BACKPLANE CONNECTIONS

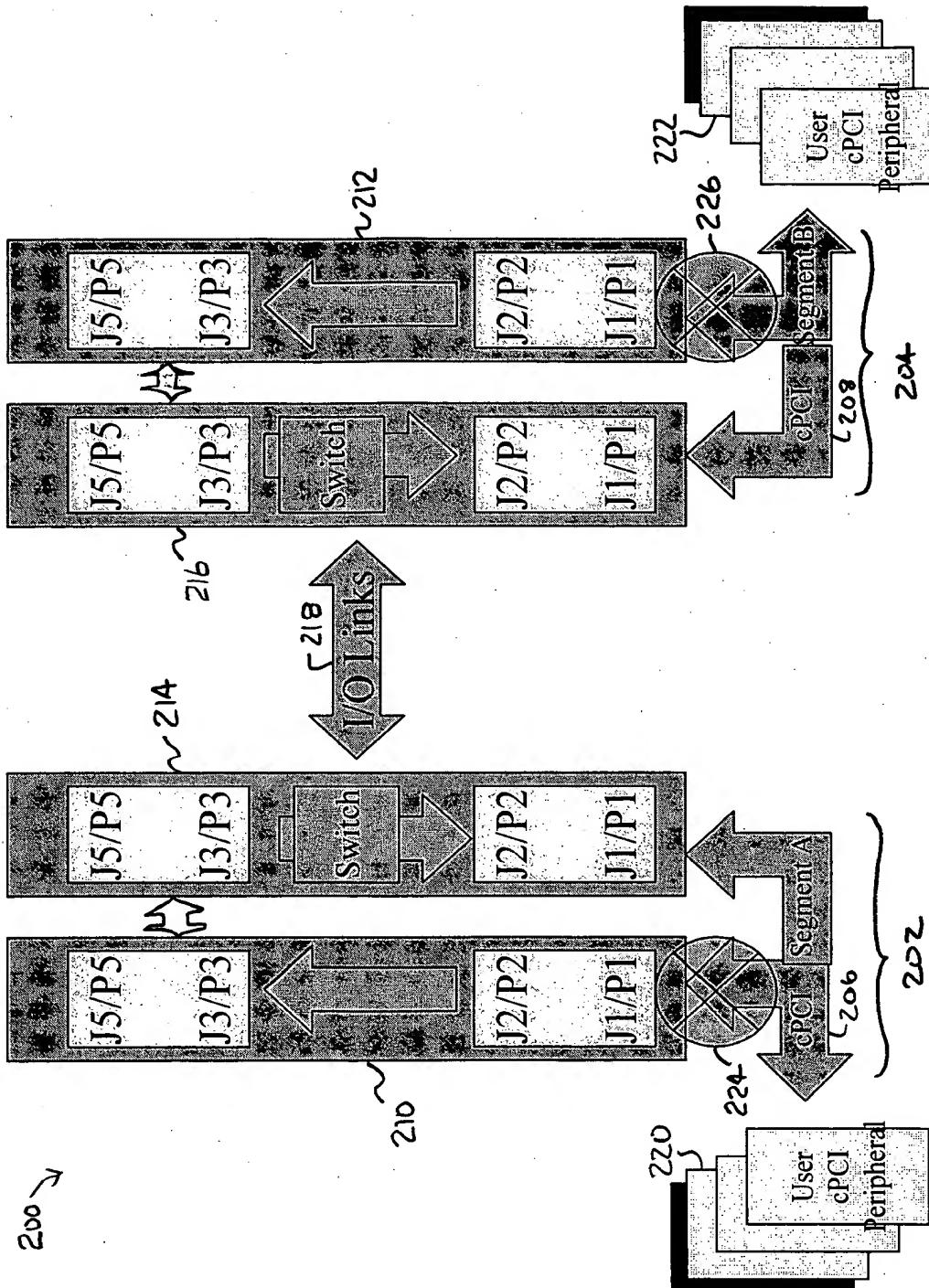
Fig. 1



cPCI SIGNALS ROUTING

Fig. 2

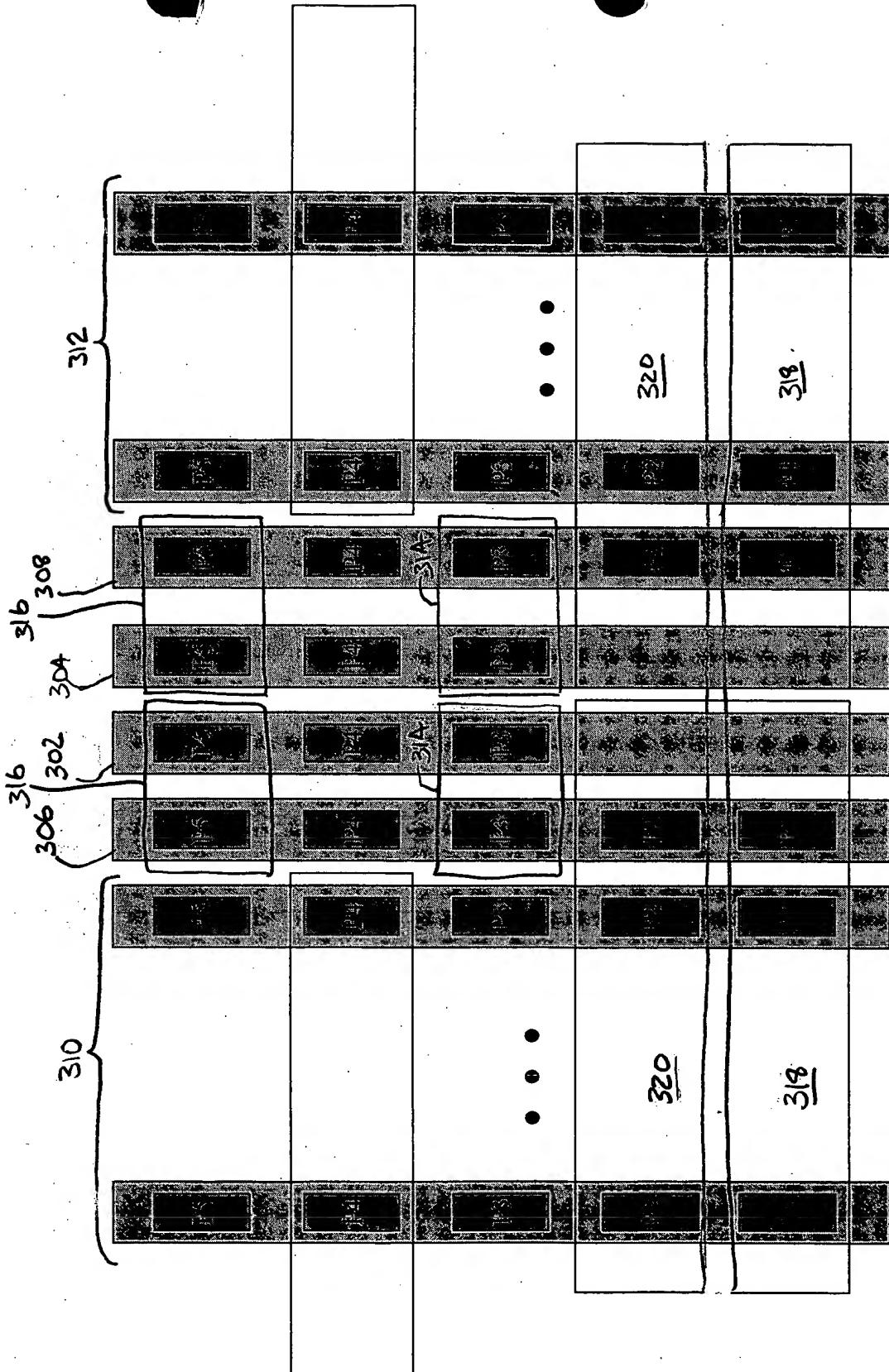
System Host HSM



BACKPLANE DESIGN

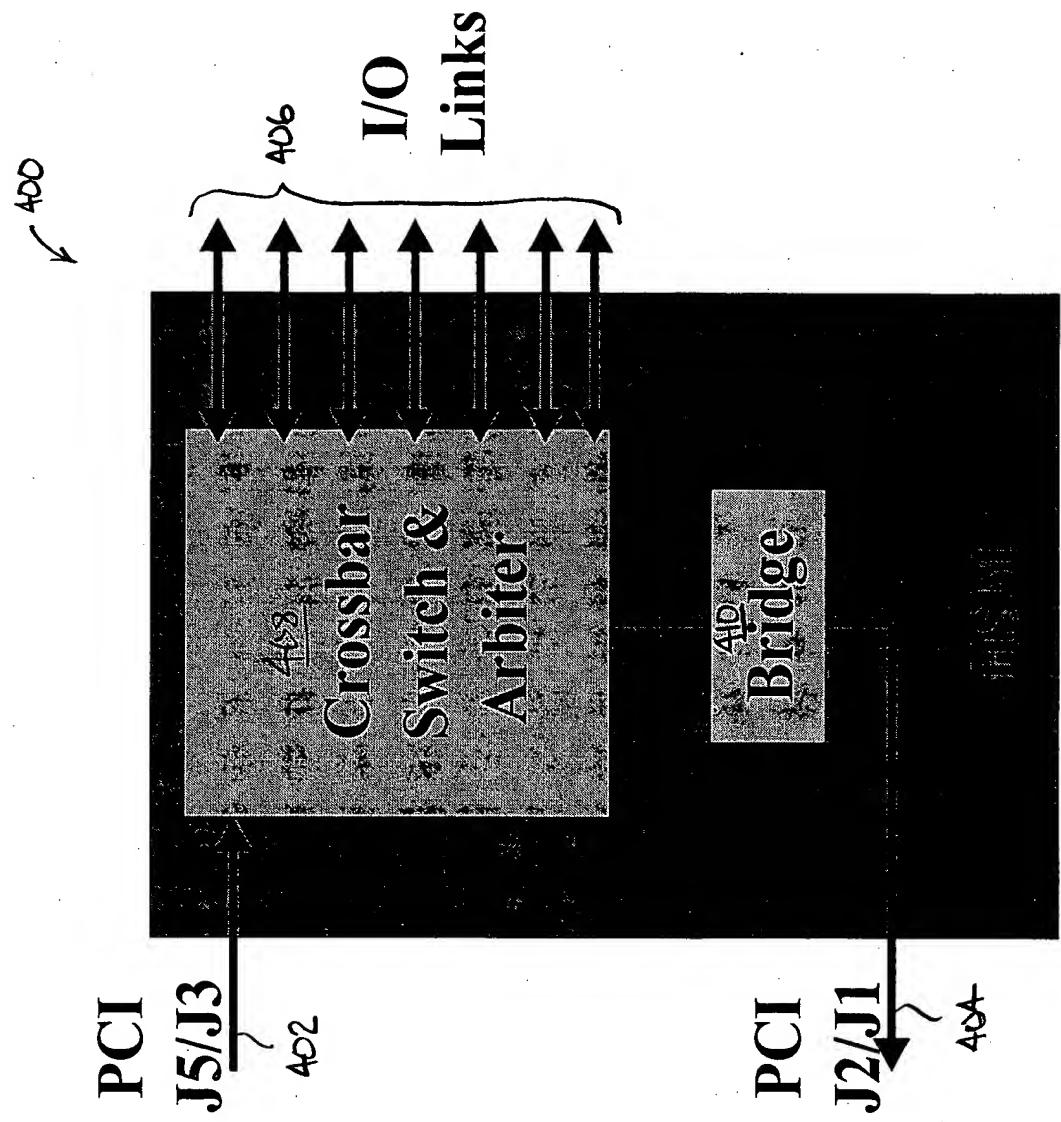
300 →

Fig.3



Hybrid Switching Module

Fig.4



PCI-IIIB Migration

Fig.5

